DELHI TECHNOLOGICAL UNIVERSITY
Established by Govt. of Delhi vide Act 6 of 2009
(FORMERLY DELHI COLLEGE OF ENGINEERING)
BAWANA ROAD, DELHI-110042

Advt. No. **DTU-MeitY-MCEV/EED/05**   21st April 2022

ADVERTISEMENT TO FILL UP PROJECT POSITION

A walk-in-interview for filling of project position as per the details given below for the MeitY Sponsored R&D Project under Chief Investigator – Prof. Mini Sreejeth, Electrical Engineering Department (EED), Delhi Technological University (DTU) and Co-investigator – Prof. Indu S, Department of Electronics and Communication Engineering, DTU is scheduled to be held on **9th May 2022** in the Conference Hall of the EED.

1. **Title of the Project**   :   "Development and Deployment of motor controller for low to medium Power Electric Vehicles"

2. **Project Sponsoring Agency**   :   Ministry of Electronics and Information Technology (MeitY). Project presently sanctioned up to 12th August 2022.

3. **Details of Project Position**

Sl. No	Désignation	Minimum Qualification	Experience	Nos.	Monthly Emoluments (₹)
b)	Senior Research Fellow (SRF)	a) B.E. / B. Tech. in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering **AND** M.E. / M. Tech. (preferably in Power Electronics) b) 1st Division at B.E./ M.E. levels **Desirable**: NET/ GATE Qualified	At least 1year experience in power electronics and electronics hardware design as per details given in Job Description	1	28,000 + 30% HRA

4. **Age Limit**   :   35 years. Age relaxation for candidates belonging to SC/ST/PH/OBC categories as per government norms.

5. **Duration**   :   Appointment to the above post shall **initially** be for three-month probation or for the duration of the project, whichever is earlier.

6. **Job description**

- Embedded software development on 32 bits ST Microelectronics microcontroller platform
- Software testing, debugging and project documentation.
- Power electronics and electronics hardware design.
- Testing and validation any EV - BLDC motor related product – Charger/ Controller/ VCU/ Telematics unit.
- Data capturing using Mode Bus/CAN Bus
- Operating test instruments like Multimeter, Digital storage Oscilloscope, Power Supply etc.
- **Note:** **Project is currently in the testing Phase. The completion of the research project being time-bound, preference will be given to candidates who are willing to join the Project with immediate effect and willing to work beyond the call of normal duty.**

(Prof. Mini Sreejeth)
Chief Investigator
MeitY Sponsored R&D Project
EED, DTU

(Prof. Indu S)
Co-Chief Investigator
MeitY Sponsored R&D Project
ECED, DTU

(Prof. Uma Nangia)
HoD, EED, DTU

7. Candidates before appearing for the Walk in Interview shall ensure that they are eligible for the position they intend to apply.

8. Candidates applying for the above post are requested to report on **9th May 2022** at **10:30 am** for registration, verification of documents, test / interview at the **Department of Electrical Engineering, DTU**. Candidates reporting **after 11:00 am will not be entertained**.

9. Candidates desirous to appear for the Walk in Interview should bring a set of self-attested copies of all certificates, testimonials, the latest CV and a passport sized colour photograph along with the original documents. The Application should include: -

   - Details of degree/certificates obtained starting from Class X (or equivalent) clearly indicating the aggregate percentage / CGPA

   - Experience in research, industrial field, academics and others.

10. Candidate shall bring along with them the **original degree(s)/certificate(s) and experience certificate(s)** at the time of walk-in interview for verification.

11. Please note that no TA/DA is admissible for attending the walk-in interview.

12. The interview will be held in **Conference Hall of Department of Electrical Engineering, DTU on 9th May 2022** at **11:00 am**.

13. The competent authority reserves the right to reject the candidature of any or all of the candidates without assigning any reason thereof.

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