



# DELHI TECHNOLOGICAL UNIVERSITY

Established under Govt. of Delhi Act 6 of 2009  
(Formerly Delhi College of Engineering)  
Shahbad Daultapur, Bawana Road, Delhi-110042

No. F. DTU/Examination /019/2018-19/ 5662 - 5668

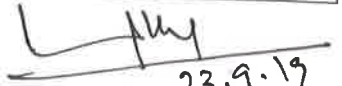
Dated: 23.09.2019

## ADDENDUM

**SUBJECT: Date Sheet for Mid Term Examination, September- 2019.**

In reference to Letter No. F.DTU/Examination/2019/5449-5458 dated. 04.09.2019, the following amendment has been made in the **Date-Sheet of B.Tech, Mid Term Examination, September-2019:**

Prog.	Course Code	Course Title	Existing Date & Time		Amended Date & time	
B.Tech.	EC-313	Microprocessors and Interfacing	26.09.2019	09:00 AM to 10:30 AM	29.09.2019	09:00 AM to 10:30 AM

  
23.9.19

(Kamal Pathak)

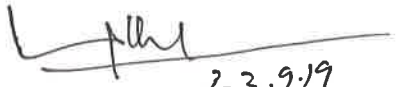
Controller of Examinations

No. F. DTU/Examination /019/2018-19/ 5662 - 5668

Dated: 23.09.2019

Copy to:

1. PA to Vice Chancellor for information of the Hon'ble Vice Chancellor.
2. PA to Pro VC for information of the Pro- Vice Chancellor.
3. Registrar, DTU
4. Head, CC: With the request to upload the date sheet on University website.
5. Centre Superintendent, Theory Examination 2018-19
6. In-Charge (Result/Secrecy/Conduct & Evaluation)
7. Guard File.

  
23.9.19

(Kamal Pathak)

Controller of Examinations