



DELHI TECHNOLOGICAL UNIVERSITY
(Formerly Delhi College of Engineering)
Shahbad Daulatpur, Main Bawana Road, Delhi-42
Academic-PG

104 /Acad.PG/Corrigendum/M.Tech. & M.Tech. by research/2025-26

Dated 16/06/2025

NOTICE

In reference to the Admission Brochure for admission in M.Tech and M.Tech By Research, Academic Year 2025-26, the revised eligibility criteria for admission to M.Tech and M.Tech by Research programs for the Academic Year 2025–26 in the **department of Electronics and Communication Engineering** is as under:

1. The details of revised eligibility conditions and qualifying degree requirements for the **M. Tech Admissions 2025-26** guidelines are as under:

i. For GATE candidates

Department of Electronics and Communication Engineering				
Program Name	Existing (published Brochure)		Revision	
	Qualifying GATE subjects	Qualifying Degree	Qualifying GATE subjects	Qualifying Degree
Microwave and Optical Communication (MOC)	EC/PH	B.E/B.Tech in ECE/EP/M.SC Electronic/M.Sc.in Physics with Electronics/Radio Physics/Solid State Physics.	EC/PH	B.E/B.Tech in ECE/EP/M.SC Electronic/M.Sc.in Physics with Electronics/Radio Physics/Solid State Physics.
Signal Processing and Digital Design (SPD)	EC	B.E/B.Tech in Electrical and Electronics/ electronics and Instrumnetation/ electronics and Computer Engg/electronics & control/Applied Electronics and Instrumentation Engg./Electronics Instrumenttation & Control Engg/ Electrical Engineering	EC/CS	B.E/B.Tech in ECE/Electrical Engineering (EE)/ Electronics Engineering/Electrical & Electronics Engineering (EEE)/Electronics & Telecommunication Engineering/Applied Electronics and Instrumentation Engg./Electronics Instrumenttation & Control Engg/ IT/CSE/SE/IS
VLSI Design and Embadded System (VLS)				

ii. For Non-GATE candidates

Department of Electronics and Communication Engineering				
Department/ Program Name	Existing (Published Brochure)		Revision	
	Qualifying Admission Test	Qualifying Degree	Qualifying Admission Test	Qualifying Degree
Microwave and Optical Communication (MOC)	DEC/DAP	B.E/B.Tech in ECE/EP/M.SC Electronic/M.Sc.in Physics with Electronics/Radio Physics/Solid State Physics.	DEC/DAP	B.E/B.Tech in ECE/EP/M.SC Electronic/M.Sc.in Physics with Electronics/Radio Physics/Solid State Physics.
Signal Processing and Digital Design (SPD)	DEC/DEE/DCS	B.E/B.Tech in Electrical and Electronics/ electronics and Instrumnetation/electronics and Computer Engg/electronics & control/Applied Electronics and Instrumentation Engg./Electronics Instrumenttation & Control Engg/ Electrical Engineering	DEC/DEE/DCS	B.E/B.Tech in ECE/ Electrical & Electronics Engineering (EEE)/Electrical Engineering (EE)/ Electronics Engineering/ /Electronics & Telecommunication Engineering/Applied Electronics and Instrumentation Engg./Electronics Instrumenttation & Control Engg/ IT/CSE/SE/IS
VLSI Design and Embadded System (VLS)				

2. The details of revised eligibility conditions and qualifying degree requirements for the **M.Tech. by Research Programme Admissions 2025-26** guidelines are as under:

i) For GATE candidates

Department of Electronics and Communication Engineering				
Program Name	Existing (published Brochure)		Revision	
	Qualifying GATE subjects	Qualifying Degree	Qualifying GATE subjects	Qualifying Degree
Microwave and Optical Communication (MOC)	EC/PH	B.E/B.Tech in ECE/EP/M.SC Electronic/M.Sc.in Physics with Electronics/Radio Physics/Solid State Physics.	EC/PH	B.E/B.Tech in ECE/EP/M.SC Electronic/M.Sc.in Physics with Electronics/Radio Physics/Solid State Physics.
Signal Processing and Digital Design (SPD)	EC	B.E/B.Tech in Electrical and Electronics/ electronics and Instrumnetation/ electronics and Computer Engg/electronics & control/Applied Electronics and Instrumentation Engg./Electronics Instrumenttation & Control Engg/ Electrical Engineering	EC/CS	B.E/B.Tech in ECE/Electrical Engineering (EE)/ Electronics Engineering/Electrical & Electronics Engineering (EEE)/Electronics & Telecommunication Engineering/Applied Electronics and Instrumentation Engg./Electronics Instrumenttation & Control Engg/ IT/CSE/SE/IS
VLSI Design and Embadded System (VLS)				

ii) For Non GATE candidates

Department of Electronics and Communication Engineering				
Department/ Program Name	Existing (Published Brochure)		Revision	
	Qualifying Admission Test	Qualifying Degree	Qualifying Admission Test	Qualifying Degree
Microwave and Optical Communication (MOC)	DEC/DAP	B.E/B.Tech in ECE/EP/M.SC Electronic/M.Sc.in Physics with Electronics/Radio Physics/Solid State Physics.	DEC/DAP	B.E/B.Tech in ECE/EP/M.SC Electronic/M.Sc.in Physics with Electronics/Radio Physics/Solid State Physics.
Signal Processing and Digital Design (SPD)	DEC/DEE/DCS	B.E/B.Tech in Electrical and Electronics/ electronics and Instrumnetation/ electronics and Computer Engg/electronics & control/Applied Electronics and Instrumentation Engg./Electronics Instrumenttation & Control Engg/ Electrical Engineering	DEC/DEE/DCS	B.E/B.Tech in ECE/ Electrical & Electronics Engineering (EEE)/Electrical Engineering (EE)/ Electronics Engineering/ /Electronics & Telecommunication Engineering/Applied Electronics and Instrumentation Engg./Electronics Instrumenttation & Control Engg/ IT/CSE/SE/IS
VLSI Design and Embadded System (VLS)				

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Chairperson M.Tech and M.Tech by Research Admission 2025-26